

## REMARKS

Claims 1-12 are pending in the present application. In the Office Action, the Examiner rejected Claims 11-12 under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,978,589 (Yoon).

It is gratefully acknowledged that Claims 1-10 have been allowed.

Claim 12 has been cancelled without prejudice.

The present application, as defined by amended independent Claim 11, is drawn to a communication system in which a control unit stores a single execution file compiled to contain program code to operate each of a plurality of target boards according to a target board identification. By using a single execution file, valuable systems resources such as memory and processing steps and time can be saved.

In contrast with that which is taught by the present application, and recited in the claims, Yoon teaches a loading method of a base station in a digital cellular system in

which lower processors request files from upper processors and that “[t]he multidrop loading system distinguishes the processors from one another according to the layer or level” (e.g., see, Column 2, Lines 32-33). This implies that all processors in the same level are distinguished accordingly. For example, Yoon teaches upper, middle, and lower processors. One skilled in the art would realize that all lower processors would be distinguished according to their respective level (i.e., the lower level) and that all middle level processors would be distinguished according to their respective level (i.e., the middle level).

Regarding the Examiner’s rejection of independent Claim 11 under 35 U.S.C. §102(b), Claim 11 has been amended and is further distinguished. Accordingly, amended Claim 11 is now drawn to a system claim corresponding to the method of allowed Claim 1. Accordingly, for at least the above-stated reasons, allowance is respectfully requested. Additionally, regarding the Examiner’s assertion that Yoon (in Column 1, lines 13-24) teaches the recitation of executing the execution files for operating the plurality of target boards, after reviewing Yoon, only a reference to a conventional system that teaches distinguishing processors (as opposed to target boards) from each other according to each

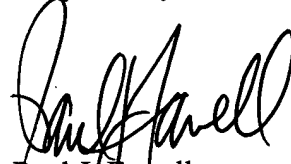
function and that a master processor runs loading by requesting a file of a corresponding processor from a hard disc was found. In other words, a plurality of files are stored which is similar to that which is described in the background section of the present application. However, Yoon does not teach or suggest the recitation of executing the execution file for operating the plurality of target boards, as recited in Claim 11. Rather, as stated above, Yoon teaches using a plurality of files to load a system. Moreover, regarding the Examiner's assertion that Yoon (in Column 1, lines 13, 24 and Column 2, lines 30-42) teaches the recitation of reading the target board ID of each target board, as recited in Claim 11, it is respectfully submitted that the Examiner is incorrect. As stated above, Yoon teaches distinguishing processors as opposed to target boards. In Column 2, lines 30-40, Yoon teaches distinguishing these processors from one another according to layer or level. However, the cited passages of Yoon do not teach or suggest the recitation of reading the target board ID of each target board, as recited in amended Claim 11.

Accordingly, for at least the above-stated reasons, it is respectfully requested that the rejection of Claim 11 under 35 U.S.C. §102(b) be withdrawn.

Independent Claim 11 is believed to be in condition for allowance.

Accordingly, all of the claims pending in the Application, namely, Claims 1-11, are believed to be in condition for allowance. Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicant's attorney at the number given below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Paul J. Farrell", written in a cursive style.

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